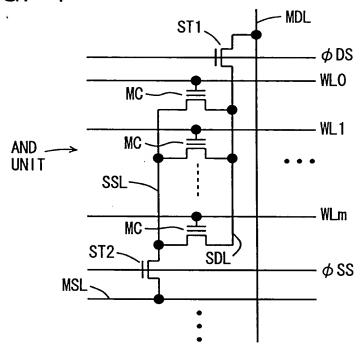
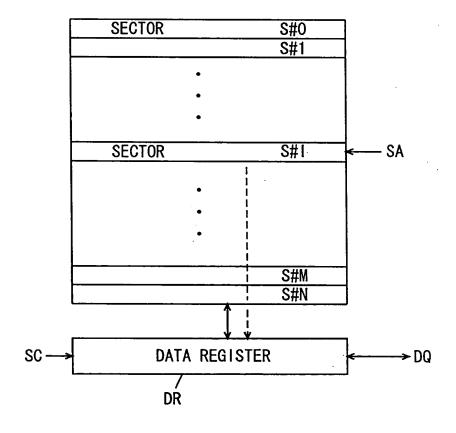
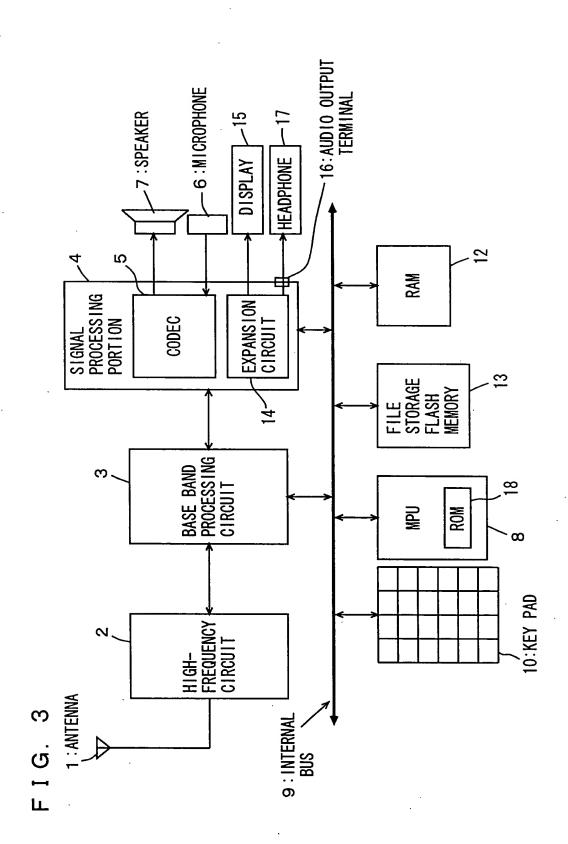
F I G. 1



F I G. 2





F I G. 4

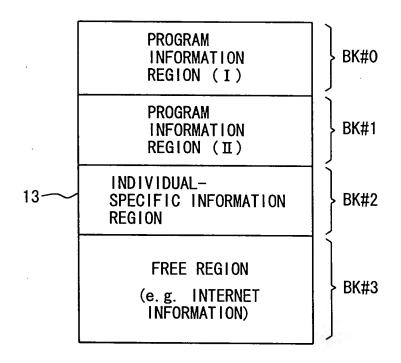
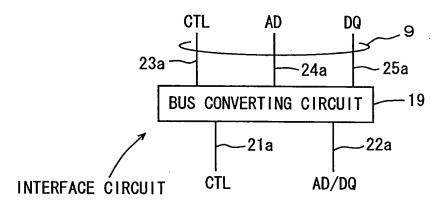


FIG. 8



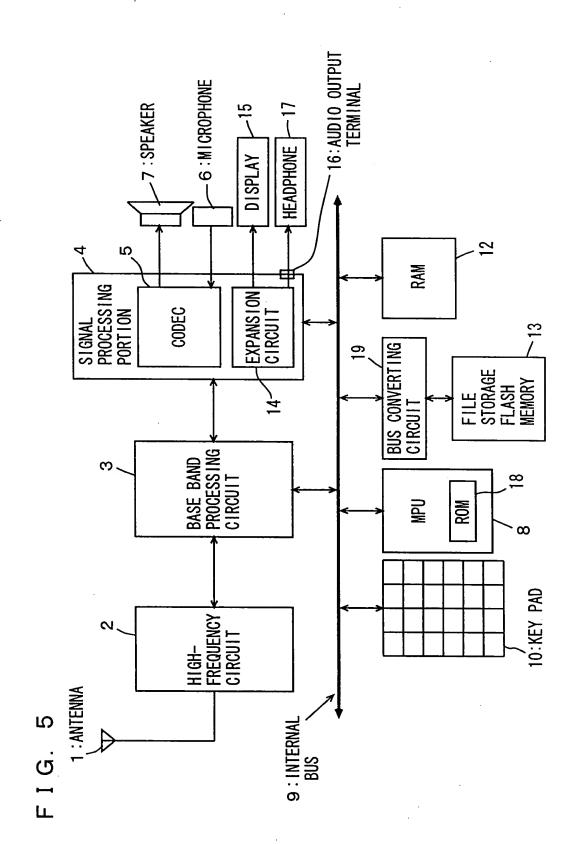


FIG. 6A

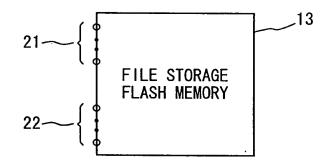
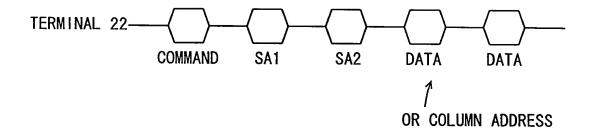
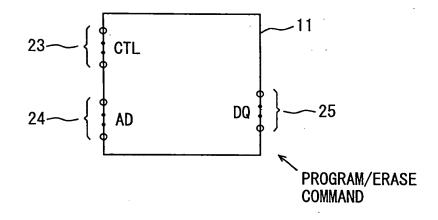


FIG. 6B



F I G. 7



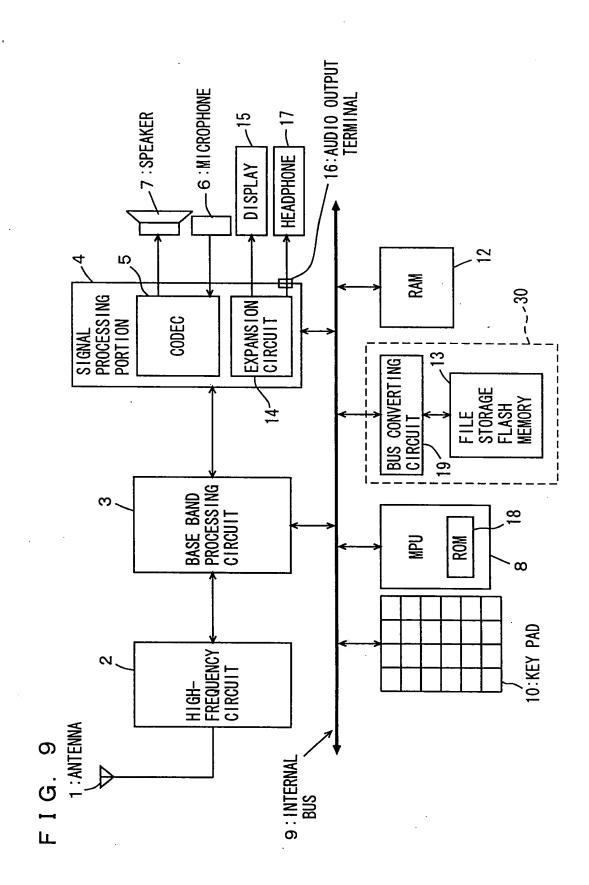


FIG. 10

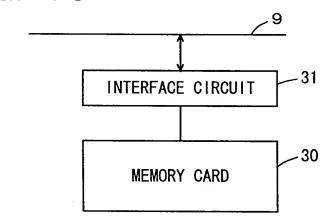


FIG. 11

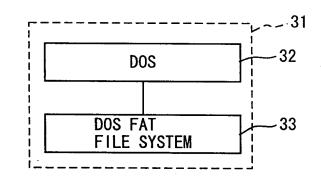
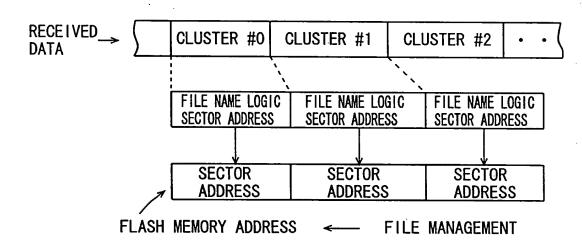
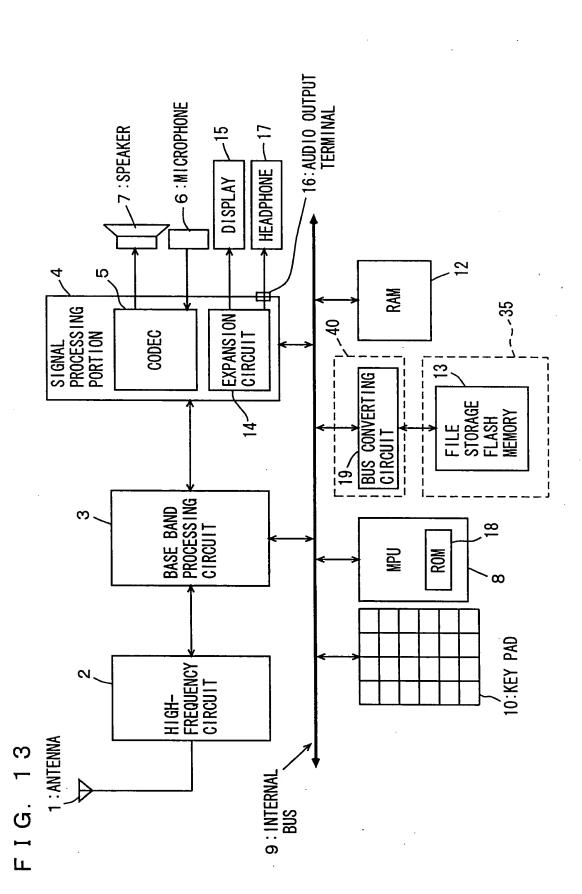
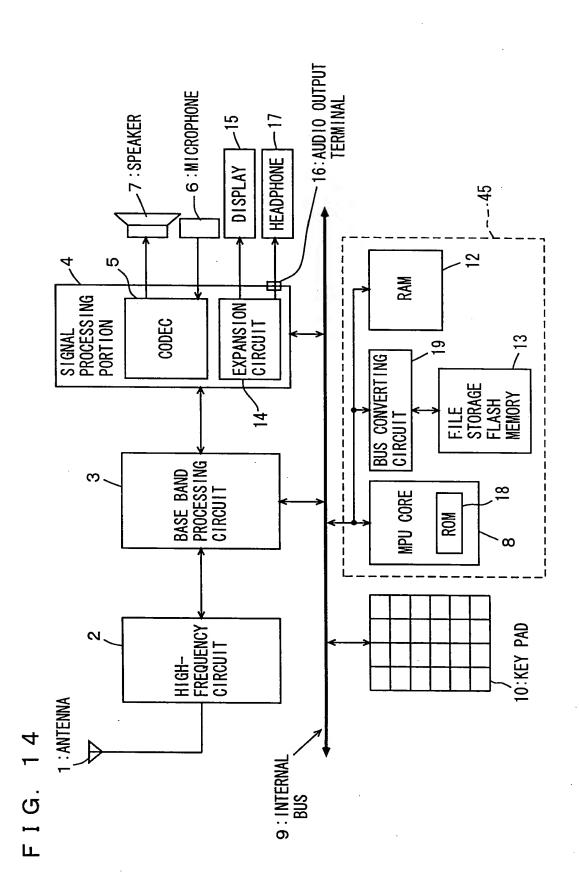


FIG. 12







-6:MICROPHONE 7 : SPEAKER സ് RAM SIGNAL PROCESSING PORTION CODEC LINEAR FLASH MEMORY BASE BAND PROCESSING CIRCUIT MPU ω 10:KEY PAD HIGH-FREQUENCY FIG. 15 PRIOR ART 1 : ANTENNA 9:INTERNAL BUS

FIG. 16 PRIOR ART

